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APPLICANTS  
 Ren Uchida, Tenri-shi, JAPAN; *Pb*

\*\* CONTINUING DATA \*\*\*\*\* *None Pb*

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *Pb*  
 JAPAN 2000-299844 09/29/2000

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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS <i>84</i>	INDEPENDENT CLAIMS <i>21</i>
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Verified and Acknowledged  
 Examiner's Signature *Pb* Initials

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TITLE  
 Testing method and testing device for semiconductor integrated circuits

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